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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## Patent Application

Applicant(s): Daniel C. Worledge

Daniel C. Worledge YOR920030410US1

Docket No.: Serial No.:

10/699,284

Filed:

October 31, 2003

Group:

2818

Examiner:

Unassigned

Title:

Techniques for Coupling in Semiconductor Devices

## **STATUS REQUEST**

Signature:

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

To date, we have received no communication or official action from the Examiner in charge of the above-identified patent application. Please let us know the status of this case.

Respectfully submitted,

Date: March 7, 2005

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